

**SWARNANDHRA**  
**COLLEGE OF ENGINEERING & TECHNOLOGY**  
**AUTONOMOUS**  
**EXAMINATION BRANCH**

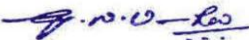
B.TECH – V SEMESTER (R19) REGULAR (Re-admitted) & SUPPLEMENTARY EXAMINATIONS, NOV - 2023

**TIME TABLE**

TIME : 10.00AM to 01.00PM

Branch	Date / Day					
	07-11-2023 TUESDAY	09-11-2023 THURSDAY	15-11-2023 WEDNESDAY	17-11-2023 FRIDAY	20-11-2023 MONDAY	22-11-2023 WEDNESDAY
CIVIL ENGINEERING ( CE – 01 )	Structural Analysis-II (19CE5T01)	Design and Drawing of Reinforced Concrete Structures (19CE5T02)	Water Resources Engineering (19CE5T03)	Geotechnical Engineering (19CE5T04)	Non-Conventional Energy Sources (19EE5O02) / Electrical Vehicle (19EE5O03) / Disaster Management (19CE5O01) / Nanotechnology and Applications (19EC5O01) / Mobile Communication and Applications (19EC5O04) / Innovations and Entrepreneurship (19BM5O01) / Industrial	Transportation Engineering (19CE5T05)
ELECTRICAL & ELECTRONICS ENGINEERING ( EEE – 02 )	Power Generation and Transmission Systems (19EE5T01)	Linear IC Applications (19EC5T04)	Principles of Economics and Management (19BM5T02)	Industrial Electrical System (19EE5E01)		--
MECHANICAL ENGINEERING ( ME – 03 )	Design of Machine Elements (19ME5T01)	Machine Tools (19ME5T02)	Thermal Engineering (19ME5T03)	Tool Design (19ME5E03) / Production Planning and Control (19ME5E04) / Operating Systems Concepts (19CS5E19)	Sociology and Psychology (19BM5O02) / Digital Marketing (19BM5O03)	Machine Drawing (19ME5T04)
ELECTRONICS & COMMUNICATION ENGINEERING ( ECE – 04 )	Linear and Digital IC Applications (19EC5T01)	Microprocessor and Microcontroller (19EC5T02)	Antennas and Wave Propagation (19EC5T03)	Operating Systems Concepts (19CS5E19)		--
COMPUTER SCIENCE AND ENGINEERING ( CSE – 05 )	Computer Networks (19CS5T01)	Object Oriented Analysis and Design (19CS5T02)	Data Science (19CS5T03)	Automata Theory and Compiler Design (19CS5T04)		Software Project Management (19CS5E03)
INFORMATION TECHNOLOGY ( IT – 12 )	Artificial Intelligence (19IT5T01)	Operating Systems (19IT5T02)	Advanced Java and Web Technologies (19IT5T03)	Compiler Design (19IT5E01)		--

Date : 09-10-2023  
Narsapur.

  
G.N.Visweswara Rao  
Controller of Examinations

  
Dr. S.Suresh Kumar  
Principal